



Document titling to DP adapter board		TopLevel.SchDoc	Revision:
Project: Ring 1 to DP adapter board		More info: -	v1.0
Author: UZH			2019-07-09
Team: UZH	Last modified: 1/19/2021	Timestamp: 4:18:57 PM 4/14/2021	Page: * / *

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PCB file name: 4 Layer PCB template.PcbDoc	Revision date: 2019-07-09	

BUILD STATISTICS	
GENERAL	
NETS	60
COMPONENTS	34
PRIMITIVES	
FILLS	0
TRACKS	1530
STRINGS	88
ARC'S	24
LANDS	
PADS	419
VA'S	204
PRINT SCALE	
SCALE	SCALE: 1.00
PRINT DATE	4/14/2021
PRINT TIME	4:18:58 PM

