



Document titling to DP adapter board		TopLevel.SchDoc	Revision:
Project: Ring 1 to DP adapter board		More info: -	v1.0
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Team: UZH	Last modified: 8/18/2020	Timestamp: 10:26:21 AM 2/11/2021	Page: 1 / 1

BUILD STATISTICS

GENERAL	
NETS	48
COMPONENTS	18
PRIMITIVES	
FLTS	0
TRACKS	1168
STRINGS	53
ARCS	26
LANDS	
PADS	239
AVS	89
PRINT SCALE	
SCALE	SCALE: 1:00
PRINT DATE	2/1/2021
PRINT TIME	10:26:23 AM

