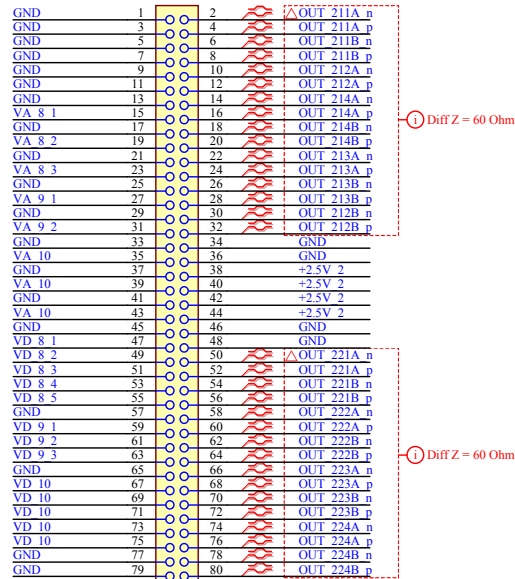
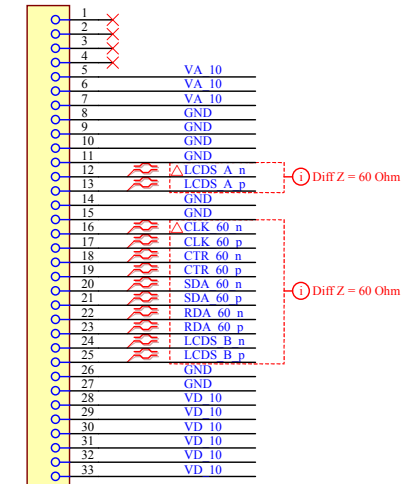


CON100
Molex 53748-0808
AdapterBoard L1&L2, CON5

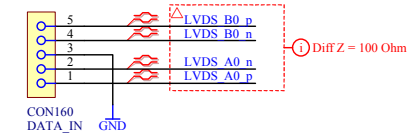


CON110
Molex 53748-0808
AdapterBoard L1&L2, CON4

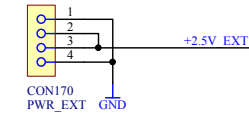
NOTE:
To simplify the layout, the signal names LCDS_A and LCDS_B don't match the pin numbering of a standard module (LCDS_A and LCDS_B reversed).



CON140
Molex 502598-3393
L2 Module Connector

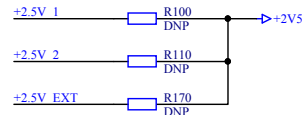
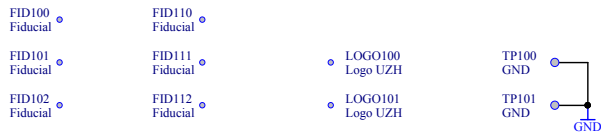


CON160
DATA_IN

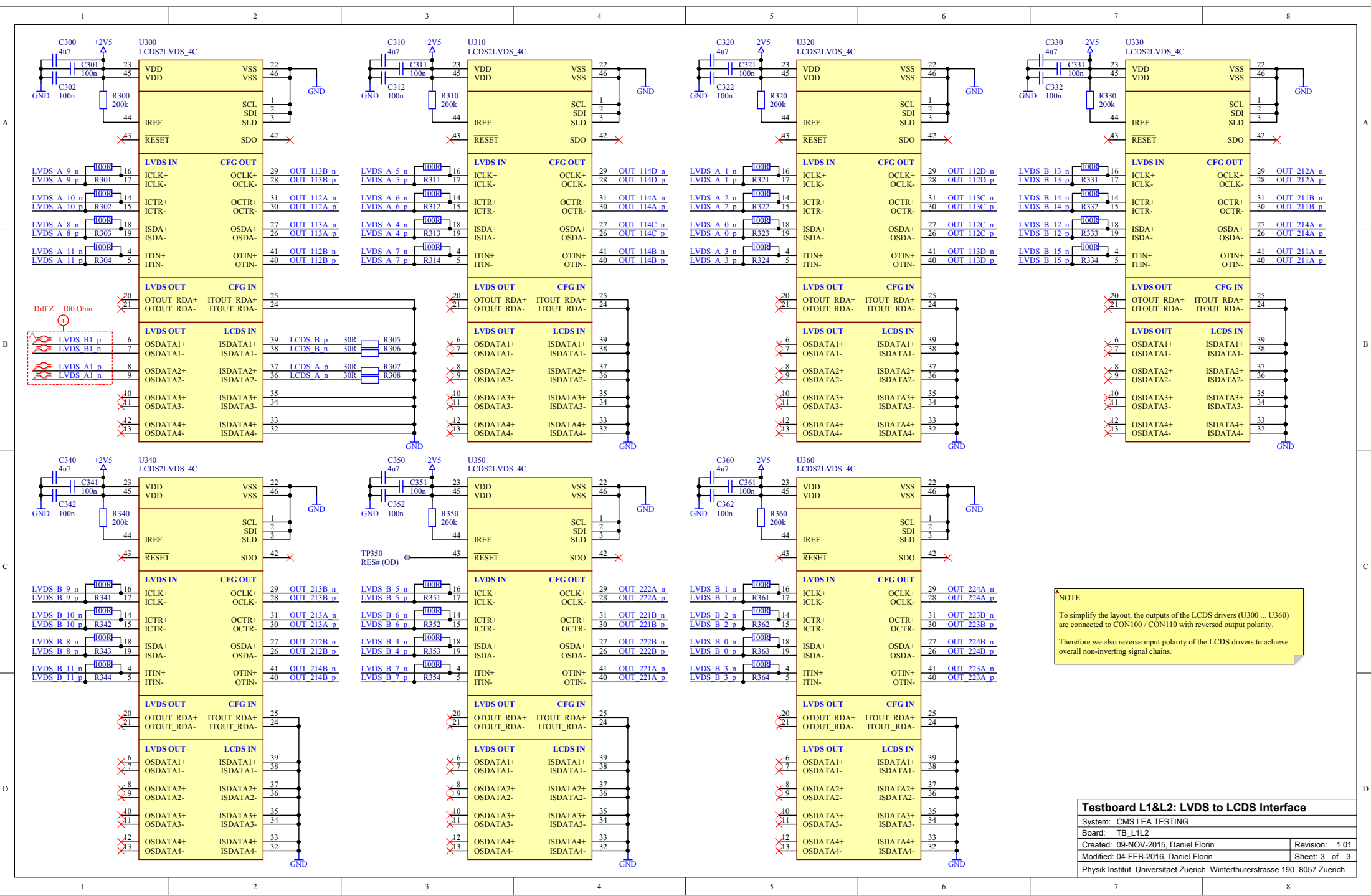


CON170
PWR_EXT

NOTE:
Board power supply either via CON100 and CON110
or via CON170 (external power supply, +2.5 V regulated).
If powered via CON100 and CON110: R100 and R110 = 0R, R170 = OPEN
If powered via CON170: use R100 and R110 = OPEN, R170 = 0R



Testboard L1&L2: Connectors	
System: CMS LEA TESTING	
Board: TB_L1L2	
Created: 09-NOV-2015, Daniel Florin	Revision: 1.01
Modified: 04-FEB-2016, Daniel Florin	Sheet: 1 of 3
Physik Institut Universitaet Zuerich Winterthurerstrasse 190 8057 Zuerich	



NOTE:

To simplify the layout, the outputs of the LCDS drivers (U300 ... U360) are connected to CON100 / CON110 with reversed output polarity.

Therefore we also reverse input polarity of the LCDS drivers to achieve overall non-inverting signal chains.

Testboard L1&L2: LVDS to LCDS Interface	
System: CMS LEA TESTING	
Board: TB_L1L2	
Created: 09-NOV-2015, Daniel Florin	Revision: 1.01
Modified: 04-FEB-2016, Daniel Florin	Sheet: 3 of 3
Physik Institut Universitaet Zuerich Winterthurerstrasse 190 8057 Zuerich	