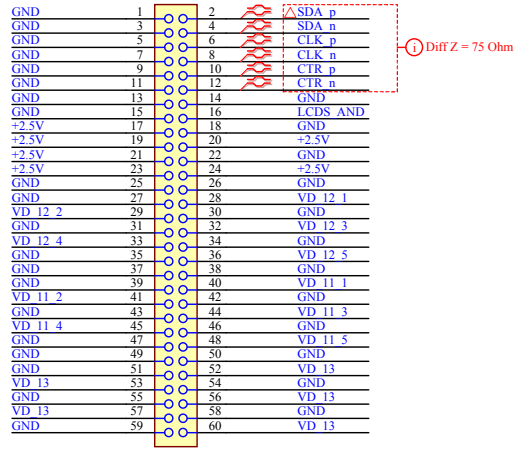
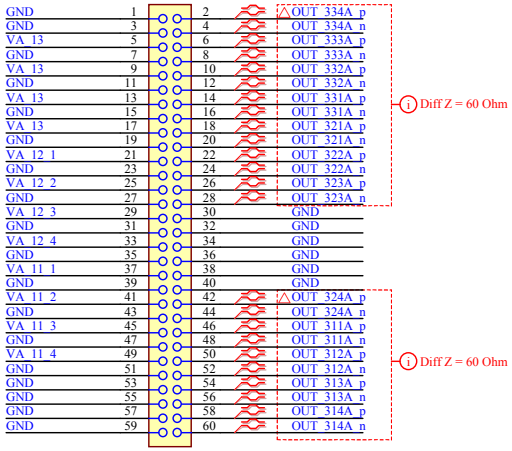
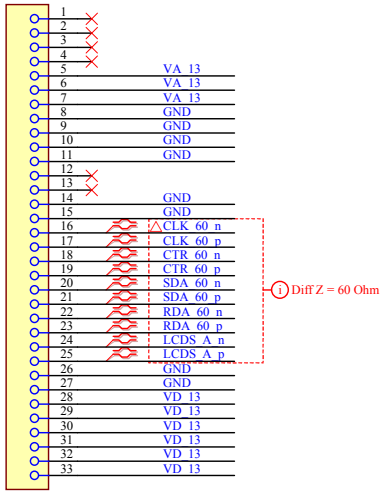


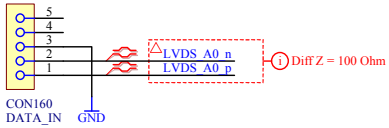
CON100
Molex 53748-0608
AdapterBoard L3&L4, CON5



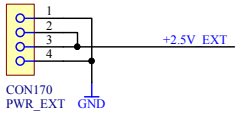
CON110
Molex 53748-0608
AdapterBoard L3&L4, CON6



CON140
Molex 502598-3393
L3 (L2) Module Connector

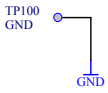


CON160
DATA_IN GND



CON170
PWR_EXT GND

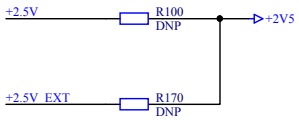
FID100 Fiducial
FID101 Fiducial
FID102 Fiducial
FID110 Fiducial
FID111 Fiducial
FID112 Fiducial



NOTE:

Board power supply either via CON100
or via CON170 (external power supply, +2.5 V regulated).

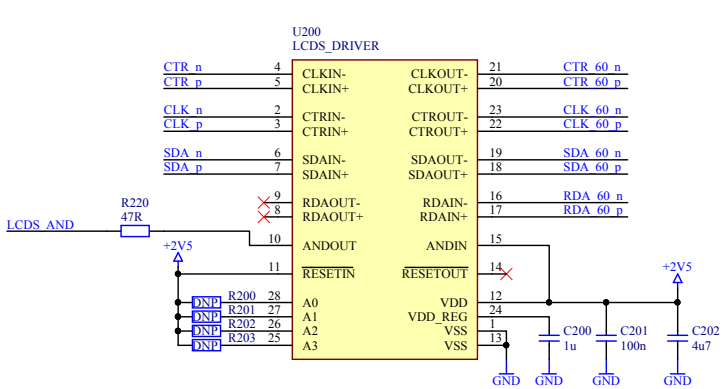
If powered via CON100: R100 = 0R, R170 = OPEN
If powered via CON170: use R100 = OPEN, R170 = 0R

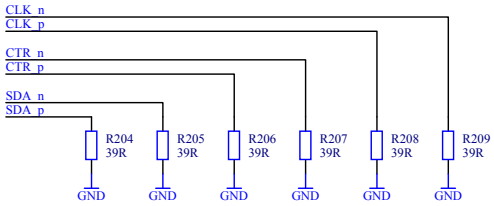


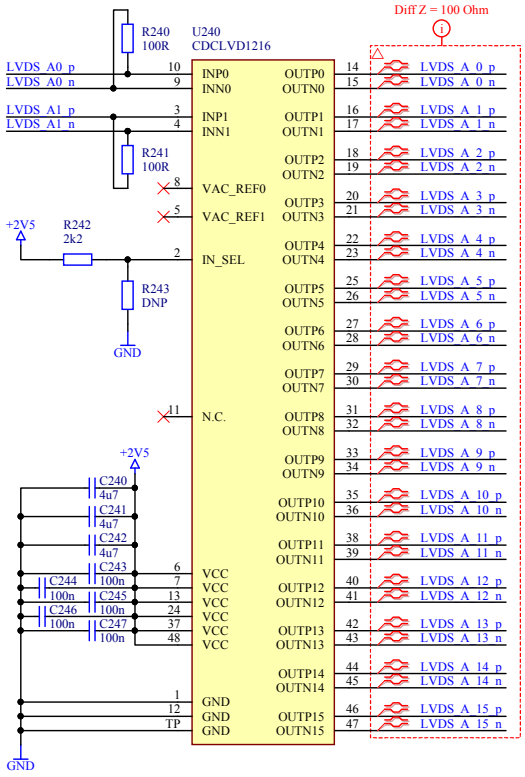
Testboard L3: Connectors	
System: CMS LEA TESTING	
Board: TB_L3	
Created: 21-JAN-2016, Daniel Florin	Revision: 1.01
Modified: 04-FEB-2016, Daniel Florin	Sheet: 1 of 3
Physik Institut Universitaet Zuerich Winterthurerstrasse 190 8057 Zuerich	

NOTE:

To simplify the layout, not all signal names match the pin names of LCDS driver U200.







NOTE:

Signal pairs LVDS_A_12 to LVDS_A_15 are not used.

Testboard L3: LVDS Buffers

System: CMS LEA TESTING	
Board: TB_L3	
Created: 21-JAN-2016, Daniel Florin	Revision: 1.01
Modified: 04-FEB-2016, Daniel Florin	Sheet: 2 of 3
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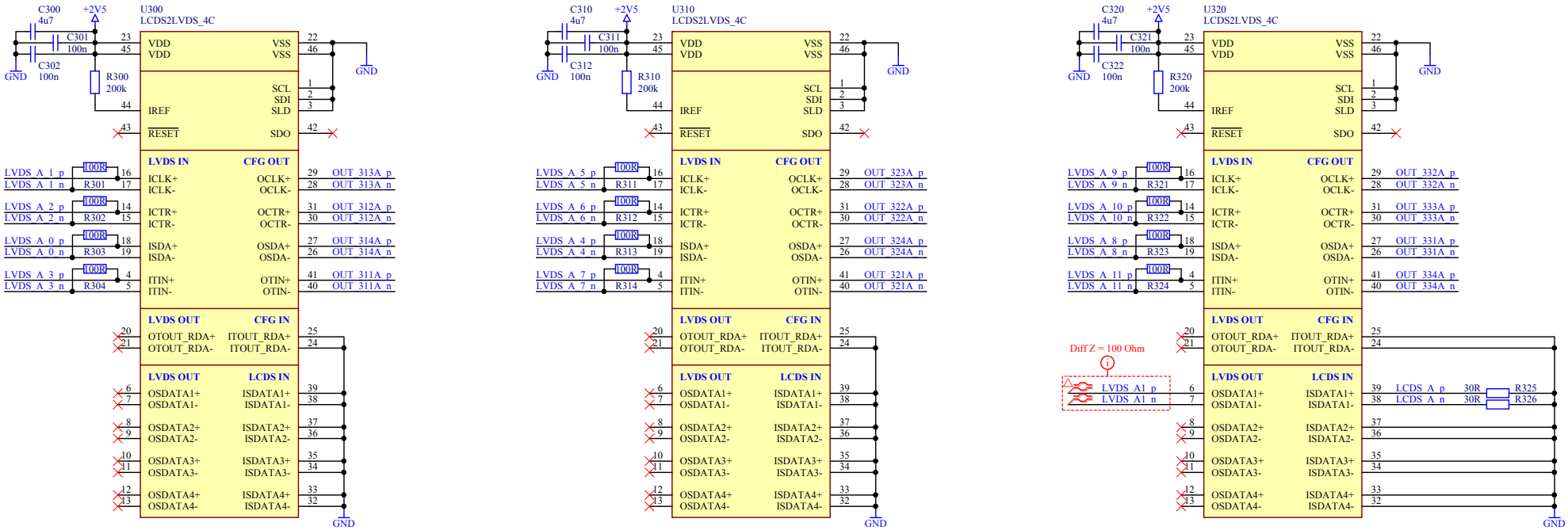

Testboard L3: LVDS to LCDS Interface	
System: CMS LEA TESTING	
Board: TB_L3	
Created: 21-JAN-2016, Daniel Florin	Revision: 1.01
Modified: 04-FEB-2016, Daniel Florin	Sheet: 3 of 3
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