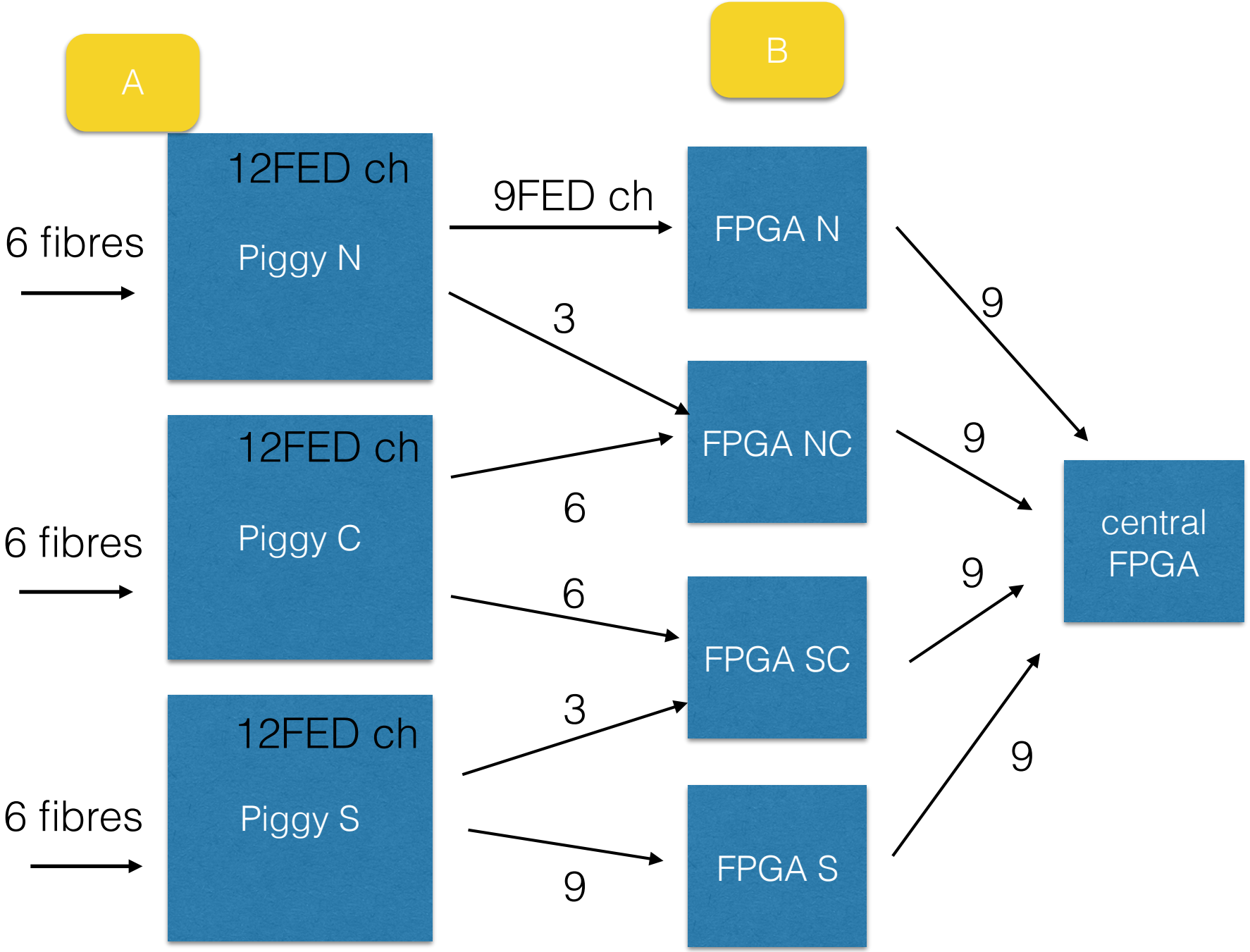

1 fibre = 2 TBM core = 2 FEDch

Fiber -> FED channel assignment at point : A

Fiber : piggy input	FED ch	
N-7	1	2
N-8	3	4
N-9	5	6
N-10	7	8
N-11	9	10
N-12	11	12
C-7	13	14
C-8	15	16
C-9	17	18
C-10	19	20
C-11	21	22
C-12	23	24
S-7	25	26
S-8	27	28
S-9	29	30
S-10	31	32
S-11	33	34
S-12	35	36

Enable/Disable fed channel at Point : B

```
VMEout(0x3, LAD_N  + 0x1a0000, 4, 0x1ff);
VMEout(0x3, LAD_NC + 0x1a0000, 4, 0x1ff);
VMEout(0x3, LAD_SC + 0x1a0000, 4, 0x1ff);
VMEout(0x3, LAD_S  + 0x1a0000, 4, 0x1ff);
```

9 bits per each FPGA

The 9 bits correspond to 9 channels which the FPGA deal with.

N	9	8	7	6	5	4	3	2	1
NC	18	17	16	15	14	13	12	11	10
SC	27	26	25	24	23	22	21	20	19
S	36	35	34	33	32	31	30	29	28

Set
0 : to enable the channel
1 : to disable the channel

For example : if I use fibre-7 on North Piggy,
it corresponds to FED ch=1,2 (previous page),
and set bit-1 and -2 zero and others 1 for LAD_N + 0x1a0000.

```
VMEout(0x3, LAD_N  + 0x1a0000, 4, 0x1fc);
VMEout(0x3, LAD_NC + 0x1a0000, 4, 0x1ff);
VMEout(0x3, LAD_SC + 0x1a0000, 4, 0x1ff);
VMEout(0x3, LAD_S  + 0x1a0000, 4, 0x1ff);
```