PixelDTB ROC/Module Interface Connector
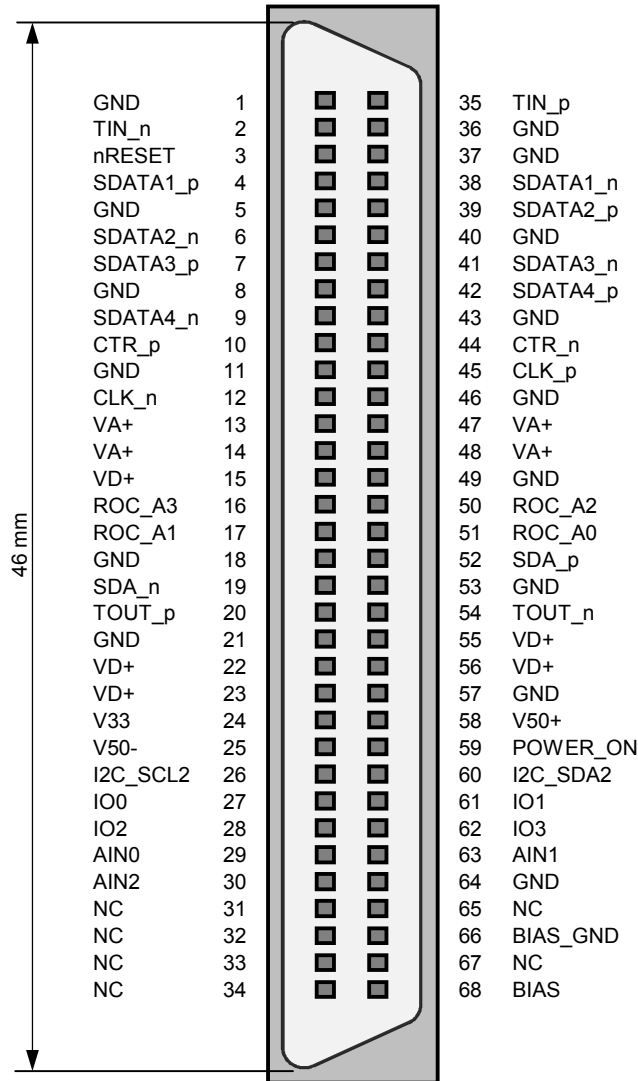
Connector Type: 68-Pin SCSI III Fast/Wide Female

Conn	Cable	Signal Name	Type	Description
1	1	GND	power	ground
35	2	TIN_p	LCDS out	token in (pos)
2	3	TIN_n	LCDS out	token in (neg)
36	4	GND	power	ground
3	5	nRESET	out	ROC reset not
37	6	GND	power	ground
4	7	SDATA1_p	LCDS in	serial data channel 1 (pos); 400Mb/s, 160Mb/s, analog
38	8	SDATA1_n	LCDS in	serial data channel 1 (neg); 400Mb/s, 160Mb/s, analog
5	9	GND	power	ground
39	10	SDATA2_p	LCDS in	serial data channel 2 (pos); 400Mb/s, analog
6	11	SDATA2_n	LCDS in	serial data channel 2 (neg); 400Mb/s, analog
40	12	GND	power	ground
7	13	SDATA3_p	LCDS in	serial data channel 3 (pos); 400Mb/s
41	14	SDATA3_n	LCDS in	serial data channel 3 (neg); 400Mb/s
8	15	GND	power	ground
42	16	SDATA4_p	LCDS in	serial data channel 4 (pos); 400Mb/s
9	17	SDATA4_n	LCDS in	serial data channel 4 (neg); 400Mb/s
43	18	GND	power	ground
10	19	CTR_p	LCDS/LVDS out	combined calibrate trigger reset signal (pos)
44	20	CTR_n	LCDS/LVDS out	combined calibrate trigger reset signal (neg)
11	21	GND	power	ground
45	22	CLK_p	LCDS/LVDS out	40 MHz clock (pos)
12	23	CLK_n	LCDS/LVDS out	40 MHz clock (neg)
46	24	GND	power	ground
13	25	VA+	power	ROC/Module analog voltage
47	26	VA+	power	ROC/Module analog voltage
14	27	VA+	power	ROC/Module analog voltage
48	28	VA+	power	ROC/Module analog voltage
15	29	VD+	power	ROC/Module digital voltage
49	30	GND	power	ground
16	31	ROC_A3	out	ROC address bit 3
50	32	ROC_A2	out	ROC address bit 2
17	33	ROC_A1	out	ROC address bit 1
51	34	ROC_A0	out	ROC address bit 0
18	35	GND	power	ground
52	36	SDA_p	LCDS/LVDS out	ROC/Module I2C data (pos)
19	37	SDA_n	LCDS/LVDS out	ROC/Module I2C data (neg)
53	38	GND	power	ground
20	39	TOUT_p	LCDS/LVDS in	ROC token out (pos)
54	40	TOUT_n	LCDS/LVDS in	ROC token out (neg)
21	41	GND	power	ground
55	42	VD+	power	ROC/Module digital voltage
22	43	VD+	power	ROC/Module digital voltage
56	44	VD+	power	ROC/Module digital voltage
23	45	VD+	power	ROC/Module digital voltage
57	46	GND	power	ground
24	47	V33	power	+3.3V
58	48	V50+	power	+5.0V
25	49	V50-	power	-5.0V
59	50	POWER_ON	out	ROC/Module power status signal (H=power on; L=power off)
26	51	I2C_SCL2	bidir	standard I2C master SCL to control periphery
60	52	I2C_SDA2	bidir	standard I2C master SDA to control periphery
27	53	IO0		general purpose digital in/out 0
61	54	IO1		general purpose digital in/out 1
28	55	IO2		general purpose digital in/out 2
62	56	IO3		general purpose digital in/out 3
29	57	AIN0	analog in	general purpose analog input 0 to ADC
63	58	AIN1	analog in	general purpose analog input 1 to ADC
30	59	AIN2	analog in	general purpose analog input 2 to ADC
64	60	GND	power	ground
31	61	nc		HV spacer
65	62	nc		HV spacer
32	63	nc		HV spacer
66	64	BIAS_GND	power	sensor bias return
33	65	nc		HV spacer
67	66	nc		HV spacer
34	67	nc		HV spacer
68	68	BIAS	power	sensor bias

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Left Signal	Pin		Pin	Right Signal
GND	1		35	TIN_p
TIN_n	2		36	GND
nRESET	3		37	GND
SDATA1_p	4		38	SDATA1_n
GND	5		39	SDATA2_p
SDATA2_n	6		40	GND
SDATA3_p	7		41	SDATA3_n
GND	8		42	SDATA4_p
SDATA4_n	9		43	GND
CTR_p	10		44	CTR_n
GND	11		45	CLK_p
CLK_n	12		46	GND
VA+	13		47	VA+
VA+	14		48	VA+
VD+	15		49	GND
ROC_A3	16		50	ROC_A2
ROC_A1	17		51	ROC_A0
GND	18		52	SDA_p
SDA_n	19		53	GND
TOUT_p	20		54	TOUT_n
GND	21		55	VD+
VD+	22		56	VD+
VD+	23		57	GND
V33	24		58	V50+
V50-	25		59	POWER_ON
I2C_SCL2	26		60	I2C_SDA2
IO0	27		61	IO1
IO2	28		62	IO3
AIN0	29		63	AIN1
AIN2	30		64	GND
NC	31		65	NC
NC	32		66	BIAS_GND
NC	33		67	NC
NC	34		68	BIAS

46 mm

Case on GND